# Article information:

chadyuu/riscv-chisel-book at 12efb03feaa8f38be93ff7312847db8b147703f5  
<https://github.com/chadyuu/riscv-chisel-book/tree/12efb03feaa8f38be93ff7312847db8b147703f5>

# Article summary:

1. This repository is for the book "RISC-V and Chisel Learn How to Make Your Own CPU - The First Step to Custom CPU Implementation with Open Source Instruction Set".

2. The book explains how to make a CPU using the open source instruction set (ISA) "RISC-V" developed at UC Berkeley.

3. At the end of the book, it summarizes the value brought by RISC-V, which is currently attracting attention.

# Article rating:

Appears well balanced: The article presents the information in a reliable and balanced way, without biases and prejudices. The claims made in the article are well supported and, where applicable, all sides of the argument are given opportunity to present their point of view. The article appears trustworthy and reliable.

# Article analysis:

The article is generally reliable and trustworthy in its content and claims. It provides a clear explanation of what the book is about, as well as an overview of its contents and goals. It also provides a link to the author's Docker Hub page for downloading images directly in case any changes have been made since publication that would render the Dockerfile unusable. Additionally, it includes an FAQ section that answers questions not addressed in the book itself.

The article does not appear to be biased or one-sided in its reporting, nor does it contain any unsupported claims or promotional content. All claims are supported by evidence provided within the article itself, such as links to relevant websites and resources. Furthermore, all potential risks are noted where applicable, such as when discussing FPGA implementation without going into detail on how to do so due to its complexity for beginners.

The only potential issue with this article is that it does not explore any counterarguments or present both sides equally when discussing why Chisel was chosen over other HDLs for this book. However, given that this article is simply providing an overview of what the book contains rather than engaging in a debate on language choice, this can be forgiven.

# Topics for further research:

* Chisel HDL advantages
* FPGA implementation tutorial
* Verilog vs Chisel comparison
* Chisel language syntax
* Chisel language tutorials
* Chisel language applications

# Report location:

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